

L Number	Hits	Search Text	DB	Time stamp
-	39	DOCUMENT WITH data WITH PIPELINE	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/16 11:22
-	20	5396616.uref.	USPAT	2003/04/14 08:59
-	0	707/500.ccls.	USPAT	2003/04/14 09:40
-	0	707/518.ccls.	US-PGPUB	
-	0	707/517.ccls.	USPAT	2003/04/14 09:40
-	495	715/500.ccls.	USPAT	2003/04/14 09:41
-	150	715/517.ccls.	US-PGPUB	
-	30	715/518.ccls.	USPAT	2003/04/14 09:52
-	41	herndon.xp. and PIPELINE	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/16 11:23
-	2	scan\$4 same pipeline	USPAT	2003/04/23 08:01
-	1278	scan\$4 same pipeline	USPAT	2003/04/23 08:01
-	53	(scan\$4 same pipeline) same region	USPAT	2003/04/23 08:02
-	245	(scan\$4 same pipeline) same block	USPAT	2003/04/23 08:02
-	245	(scan\$4 same pipeline) same blocks	USPAT	2003/04/23 08:02
-	63	(scan\$4 same pipeline) same segment	USPAT	2003/04/23 08:11
-	6	((scan\$4 same pipeline) same segment) same parallel	USPAT	2003/04/23 08:04
-	1	("6021256").PN.	USPAT; IBM TDB	2003/04/23 09:35
-	1	"5751862".PN.	USPAT	2003/04/23 08:16
-	1	"5745601".PN.	USPAT	2003/04/23 08:17
-	1	"5502793".PN.	USPAT	2003/04/23 08:20
-	1	"5471543".PN.	USPAT	2003/04/23 10:02
-	1	"5359431".PN.	USPAT	2003/04/23 08:42
-	1	"5130821".PN.	USPAT	2003/04/23 08:48
-	113	manual same selection same screen same image	USPAT; EPO; JPO; DERWENT; IBM TDB	2003/04/23 09:36
-	11	(manual same selection same screen same image) same type same proces\$4	USPAT; EPO; JPO; DERWENT; IBM TDB	2003/04/23 09:42
-	3	(manual same selection same screen same image) same copier	USPAT; EPO; JPO; DERWENT; IBM TDB	2003/04/23 09:42
-	2	(("4,174,514") or ("4,484,349")).PN.	USPAT; EPO; JPO; IBM TDB	2003/04/23 10:24
-	10	partition\$3 same image same pipelines same type	USPAT; EPO; JPO; DERWENT; IBM TDB	2003/04/23 10:17
-	56	4,484,349.uref.	USPAT; EPO; JPO; DERWENT; IBM TDB	2003/04/23 10:24
-	275	382/176.ccls.	USPAT	2003/04/23 13:12

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
1	US 6400789 B1	20020604	On-line image reconstruction in helical CT scanners	378/15	Dafni, Ehud	US 6400789	<input type="checkbox"/>
2	US 6393151 B1	20020521	Pattern reading system	382/200	Yamamoto, Kazuhiko et al.	US 6393151	<input type="checkbox"/>
3	US 6377273 B1	20020423	Fast area-coverage computing method for anti-aliasing in graphics	345/611	Lee, Ruen-Rone et al.	US 6377273	<input type="checkbox"/>
4	US 6269436 B1	20010731	Superscalar microprocessor configured to predict return addresses from a return stack storage	712/23	Tran, Thang M. et al.	US 6269436	<input type="checkbox"/>
5	US 6243483 B1	20010605	Mapping system for the integration and graphical display of pipeline information that enables automated pipeline surveillance	382/103	Petrou, Maria et al.	US 6243483	<input type="checkbox"/>
6	US 6240506 B1	20010529	Expanding instructions with variable-length operands to a fixed length	712/213	Miller, Paul K.	US 6240506	<input type="checkbox"/>
7	US 6189068 B1	20010213	Superscalar microprocessor employing a data cache capable of performing store accesses in a single clock cycle	711/3	Witt, David B. et al.	US 6189068	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
8	US 6172678 B1	20010109	Image processing method and apparatus including hidden surface removal	345/419	Shiraishi, Naoto	US 6172678	<input type="checkbox"/>
9	US 6122274 A	20000919	ATM switching system with decentralized pipeline control and plural memory modules for very high capacity data switching	370/388	Kumar, Sanjeev	US 6122274	<input type="checkbox"/>
10	US 6079006 A	20000620	Stride-based data address prediction structure	711/213	Pickett; James K.	US 6079006	<input type="checkbox"/>
11	US 6078697 A	20000620	Method and apparatus for segmenting image data into contone, text and halftone classifications	382/275	Ng, Yee Seung	US 6078697	<input type="checkbox"/>
12	US 6028608 A	20000222	System and method of perception-based image generation and encoding	345/619	Jenkins, Barry	US 6028608	<input type="checkbox"/>
13	US 6014734 A	20000111	Superscalar microprocessor configured to predict return addresses from a return stack storage	712/23	Tran, Thang M. et al.	US 6014734	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
14	US 5987561 A	19991116	Superscalar microprocessor employing a data cache capable of performing store accesses in a single clock cycle	711/3	Witt, David B. et al.	US 5987561	<input type="checkbox"/>
15	US 5983336 A	19991109	Method and apparatus for packing and unpacking wide instruction word using pointers and masks to shift word syllables to designated execution units groups	712/24	Sakhin, Yuli Kh. et al.	US 5983336	<input type="checkbox"/>
16	US 5978907 A	19991102	Delayed update register for an array	712/239	Tran, Thang M. et al.	US 5978907	<input type="checkbox"/>
17	US 5956706 A	19990921	Method and system for limiting the cardinality of an SQL query result	707/2	Carey, Michael James et al.	US 5956706	<input type="checkbox"/>
18	US 5935239 A	19990810	Parallel mask decoder and method for generating said mask	712/224	Narayan, Rammohan	US 5935239	<input type="checkbox"/>
19	US 5933618 A	19990803	Speculative register storage for storing speculative results corresponding to register updated by a plurality of concurrently recorded instruction	712/217	Tran, Thang M. et al.	US 5933618	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
20	US 5892936 A	19990406	Speculative register file for storing speculative register states and removing dependencies between instructions utilizing the register	712/216	Tran, Thang M. et al.	US 5892936	<input type="checkbox"/>
21	US 5881278 A	19990309	Return address prediction system which adjusts the contents of return stack storage to enable continued prediction after a mispredicted branch	712/242	Tran, Thang M. et al.	US 5881278	<input type="checkbox"/>
22	US 5878255 A	19990302	Update unit for providing a delayed update to a branch prediction array	712/240	Tran, Thang M. et al.		<input type="checkbox"/>
23	US 5875324 A	19990223	Superscalar microprocessor which delays update of branch prediction information in response to branch misprediction until a subsequent idle clock	712/238	Tran, Thang M. et al.		<input type="checkbox"/>
24	US 5875315 A	19990223	Parallel and scalable instruction scanning unit	712/204	Narayan, Rammohan		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
25	US 5864707 A	19990126	Superscalar microprocessor configured to predict return addresses from a return stack storage	712/23	Tran, Thang M. et al.		<input type="checkbox"/>
26	US 5860104 A	19990112	Data cache which speculatively updates a predicted data cache storage location with store data and subsequently corrects mispredicted updates	711/137	Witt, David B. et al.		<input type="checkbox"/>
27	US 5859991 A	19990112	Parallel and scalable method for identifying valid instructions and a superscalar microprocessor including an instruction scanning unit employing the method	712/204	Narayan, Rammohan		<input type="checkbox"/>
28	US 5854921 A	19981229	Stride-based data address prediction structure	712/239	Pickett, James K.		<input type="checkbox"/>
29	US 5844532 A	19981201	Color display system	345/89	Silverbrook, Kia et al.		<input type="checkbox"/>
30	US 5835145 A	19981110	Conversion system using programmable tables for compressing transform coefficients	375/240.2	Ouyang, Chi et al.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
31	US 5826071 A	19981020	Parallel mask decoder and method for generating said mask	712/224	Narayan, Rammohan		<input type="checkbox"/>
32	US 5822559 A	19981013	Apparatus and method for aligning variable byte-length instructions to a plurality of issue positions	712/214	Narayan, Rammohan et al.		<input type="checkbox"/>
33	US 5781789 A	19980714	Superscalar microprocessor employing a parallel mask decoder	712/23	Narayan, Rammohan		<input type="checkbox"/>
34	US 5752069 A	19980512	Superscalar microprocessor employing away prediction structure	712/23	Roberts, James S. et al.		<input type="checkbox"/>
35	US 5699117 A	19971216	Moving picture decoding circuit	348/390.1	Uramoto, Shinichi et al.		<input type="checkbox"/>
36	US 5659630 A	19970819	Advanced manufacturing inspection system	382/149	Forslund, Donald Charles		<input type="checkbox"/>
37	US 5561476 A	19961001	Motion detection method and apparatus	348/699	Kershaw, Natalie L. et al.		<input type="checkbox"/>
38	US 5481319 A	19960102	Motion detection method and apparatus	348/699	Kershaw, Natalie L. et al.		<input type="checkbox"/>
39	US 5420941 A	19950530	Parallel pipelined image processor	382/302	Capo, Joseph M. et al.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
40	US 5299577 A	19940405	Apparatus and method for image processing including one-dimensional clean approximation	600/443	Brown, Fredrick B. et al.		<input type="checkbox"/>
41	US 5287416 A	19940215	Parallel pipelined image processor	382/303	Capo, Joseph M. et al.		<input type="checkbox"/>
42	US 5253335 A	19931012	Hidden-surface processing device, anti-aliasing method and three-dimensional graphics processing apparatus	345/422	Mochizuki, Yoshiyuki et al.		<input type="checkbox"/>
43	US 5142273 A	19920825	System for generating color blended video signal	345/690	Wobermin, James A.		<input type="checkbox"/>
44	US 5131086 A	19920714	Method and system for executing pipelined three operand construct	712/213	Circello, Joseph C. et al.		<input type="checkbox"/>
45	US 5101341 A	19920331	Pipelined system for reducing instruction access time by accumulating predecoded instruction bits a FIFO	712/213	Circello, Joseph C. et al.		<input type="checkbox"/>
46	US 5077810 A	19911231	Distributed digital signal processing system using standard resolution processors for a high resolution sensor	382/323	D'Luna, Lionel J.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
47	US 5007100 A	19910409	Diagnostic system for a parallel pipelined image processing system	382/303	D'Aoust, Robert et al.		<input type="checkbox"/>
48	US 4945500 A	19900731	Triangle processor for 3-D graphics display system	345/422	Deering, Michael F.		<input type="checkbox"/>
49	US 4945479 A	19900731	Tightly coupled scientific processing system	712/3	Rusterholz, John T. et al.		<input type="checkbox"/>
50	US 4875161 A	19891017	Scientific processor vector file organization	711/157	Lahti, Archie E.		<input type="checkbox"/>
51	US 4873630 A	19891010	Scientific processor to support a host processor referencing common memory	712/3	Rusterholz, John T. et al.		<input type="checkbox"/>
52	US 4858115 A	19890815	Loop control mechanism for scientific processor	712/7	Rusterholz, John T. et al.		<input type="checkbox"/>
53	US 4823120 A	19890418	Enhanced video graphics controller	345/629	Thompson, Laurence A. et al.		<input type="checkbox"/>
54	US 4821210 A	19890411	Fast display of three-dimensional images	345/421	Rumbaugh, James E.		<input type="checkbox"/>
55	US 4805128 A	19890214	Format conversion system	708/204	Nelsen, Pete E. et al.		<input type="checkbox"/>
56	US 4789925 A	19881206	Vector data logical usage conflict detection	712/7	Lahti, Archie E.		<input type="checkbox"/>
57	US 4736291 A	19880405	General-purpose array processor	712/11	Jennings, Ronald B. et al.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
58	US 4706191 A	19871110	Local store for scientific vector processor	712/3	Hamstra, James R. et al.		<input type="checkbox"/>
59	US 4484349 A	19841120	Parallel pipeline image processor	382/308	McCubbrey, David L.		<input type="checkbox"/>
60	US 4241341 A	19801223	Apparatus for scan conversion	345/27	Thorson, Mark R.		<input type="checkbox"/>
61	US 4021925 A	19770510	Apparatus for and process of determining the internal diameter of pipelines	33/544.3	Loftus, William F.		<input type="checkbox"/>
62	US 3925776 A	19751209	Display terminal system	345/1.1	Swallow, Ronald J.		<input type="checkbox"/>
63	US 3786684 A	19740122	PIPELINE INSPECTION PIG	73/866.5	Wiers, William C. et al.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
1	US 6212303 B1	20010403	Method and module system for high speed processing of item images	358/426.16	Doran, Wayne M. et al.	US 6212303	<input type="checkbox"/>
2	US 6205248 B1	20010320	Method and system for compressing data in a multi-channel image processing system	382/232	Vieth, John O. G.	US 6205248	<input type="checkbox"/>
3	US 6145071 A	20001107	Multi-layer multi-process or information conveyor with periodic transferring of processors' states for on-the-fly transformation of continuous information flows and operating method therefor	712/18	Berkovich, Semyon et al.	US 6145071	<input type="checkbox"/>
4	US 6111994 A	20000829	Outline extraction using partial image processing	382/298	Katayama, Akihiro et al.	US 6111994	<input type="checkbox"/>
5	US 5828794 A	19981027	Image processing method and apparatus for enlarging and reducing image data	382/298	Katayama, Akihiro et al.	US 5828794	<input type="checkbox"/>
6	US 5809180 A	19980915	Data conversion system for line-narrowing a pattern	382/288	Kimura, Masayuki et al.	US 5809180	<input type="checkbox"/>
7	US 5701505 A	19971223	Image data parallel processing apparatus	712/11	Yamashita, Shigeki et al.	US 5701505	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
8	US 5689575 A	19971118	Method and apparatus for processing images of facial expressions	382/118	Sako, Hiroshi et al.	US 5689575	<input type="checkbox"/>
9	US 5659630 A	19970819	Advanced manufacturing inspection system	382/149	Forslund, Donald Charles	US 5659630	<input type="checkbox"/>
10	US 5502773 A	19960326	Method and apparatus for automated processing of DNA sequence data	382/129	Tibbetts, Clark et al.	US 5502773	<input type="checkbox"/>
11	US 5461679 A	19951024	Method and apparatus for encoding/decoding image data	382/304	Normile, James O. et al.	US 5461679	<input type="checkbox"/>
12	US 5420941 A	19950530	Parallel pipelined image processor	382/302	Capo, Joseph M. et al.	US 5420941	<input type="checkbox"/>
13	US 5390262 A	19950214	Method for splitting and configuring a multi-channel image processing system	382/234	Pope, Charles K.	US 5390262	<input type="checkbox"/>
14	US 5315692 A	19940524	Multiple object pipeline display system	345/419	Hansen, Dennis D. et al.	US 5315692	<input type="checkbox"/>
15	US 5301038 A	19940405	Image processor and method for processing pixel data	382/269	Todd, Stephen J. P.	US 5301038	<input type="checkbox"/>
16	US 5297222 A	19940322	Image processing apparatus	382/318	Mori, Kinji et al.	US 5297222	<input type="checkbox"/>
17	US 5287416 A	19940215	Parallel pipelined image processor	382/303	Capo, Joseph M. et al.	US 5287416	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
18	US 5274760 A	19931228	Extendable multiple image-buffer for graphics systems	345/506	Schneider, Bengt-Olaf	US 5274760	<input type="checkbox"/>
19	US 5274717 A	19931228	Parallel image processor for performing local neighboring image processing	382/304	Miura, Shuuichi et al.	US 5274717	<input type="checkbox"/>
20	US 5237626 A	19930817	Universal image processing module	382/256	Forslund, Donald C. et al.	US 5237626	<input type="checkbox"/>
21	US 5212742 A	19930518	Method and apparatus for encoding/decoding image data	382/166	Normile, James O. et al.	US 5212742	<input type="checkbox"/>
22	US 5187777 A	19930216	Multi-function pre-processor for target tracking	345/502	Conboy, Mark A. et al.	US 5187777	<input type="checkbox"/>
23	US 5155852 A	19921013	Digital information coding system which evenly distributes valid input data to digital signal processors operating in parallel	709/100	Murakami, Tokumichi et al.	US 5155852	<input type="checkbox"/>
24	US 5146592 A	19920908	High speed image processing computer with overlapping windows-div	345/807	Pfeiffer, David M. et al.	US 5146592	<input type="checkbox"/>
25	US 5129060 A	19920707	High speed image processing computer	345/563	Pfeiffer, David M. et al.	US 5129060	<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
26	US 5117468 A	19920526	Image processing system capable of carrying out local processing for image at high speed	382/205	Hino, Masatoshi et al.	US 5117468	<input type="checkbox"/>
27	US 5109348 A	19920428	High speed image processing computer	345/505	Pfeiffer, David M. et al.	US 5109348	<input type="checkbox"/>
28	US 5077810 A	19911231	Distributed digital signal processing system using standard resolution processors for a high resolution sensor	382/323	D'Luna, Lionel J.	US 5077810	<input type="checkbox"/>
29	US 5070531 A	19911203	Method of and means for processing image data	382/304	Schuerman, Cornelis P. et al.		<input type="checkbox"/>
30	US 5046190 A	19910903	Pipeline image processor	382/303	Daniel, Thomas et al.		<input type="checkbox"/>
31	US 5043868 A	19910827	System for by-pass control in pipeline operation of computer	712/218	Kitamura, Toshiaki et al.		<input type="checkbox"/>
32	US 5038386 A	19910806	Polymorphic mesh network image processing system	382/302	Li, Hungwen		<input type="checkbox"/>
33	US 5007100 A	19910409	Diagnostic system for a parallel pipelined image processing system	382/303	D'Aoust, Robert et al.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
34	US 4985848 A	19910115	High speed image processing system using separate data processor and address generator	345/505	Pfeiffer, David M. et al.		<input type="checkbox"/>
35	US 4972495 A	19901120	Feature extraction processor	382/304	Blike, James J. et al.		<input type="checkbox"/>
36	US 4967340 A	19901030	Adaptive processing system having an array of individually configurable processing components	712/19	Dawes, Robert L.		<input type="checkbox"/>
37	US 4949390 A	19900814	Interconnect verification using serial neighborhood processors	382/258	Iverson, R. David et al.		<input type="checkbox"/>
38	US 4864629 A	19890905	Image correlation system	382/216	Deering, Michael F.		<input type="checkbox"/>
39	US 4850027 A	19890718	Configurable parallel pipeline image processing system	382/303	Kimmel, Milton J.		<input type="checkbox"/>
40	US 4845663 A	19890704	Image processor with free flow pipeline bus	713/600	Brown, Dwight E. et al.		<input type="checkbox"/>
41	US 4829585 A	19890509	Electronic image processing circuit	382/277	Pape, David D.		<input type="checkbox"/>
42	US 4811413 A	19890307	System of reconfigurable pipelines of generalized neighborhood function morphic image processors	382/308	Kimmel, Milton J.		<input type="checkbox"/>

	Document ID	Issue Date	Title	Current OR	Inventor	Image Doc. Displayed	PT
43	US 4809347 A	19890228	Computer vision architecture	382/240	Nash, James G. et al.		<input type="checkbox"/>
44	US 4809346 A	19890228	Computer vision architecture for iconic to symbolic transformation	382/302	Shu, David B.		<input type="checkbox"/>
45	US 4807297 A	19890221	Neighbor image processing device	382/260	Hosoda, Makoto		<input type="checkbox"/>
46	US 4805227 A	19890214	Parallel pipeline image processor with 2.times.2 window architecture	382/303	Wehner, William W.		<input type="checkbox"/>
47	US 4805226 A	19890214	Image processing apparatus	382/260	Guebey, Patrick		<input type="checkbox"/>
48	US 4797806 A	19890110	High speed serial pixel neighborhood processor and method	382/303	Krich, David M.		<input type="checkbox"/>
49	US 4790026 A	19881206	Programmable pipelined image processor	382/303	Gennery, Donald B. et al.		<input type="checkbox"/>
50	US 4742552 A	19880503	Vector image processing system	382/303	Andrews, Barry A.		<input type="checkbox"/>
51	US 4740894 A	19880426	Computing processor with memoryless function units each connected to different part of a multiported memory	711/149	Lyon, Richard F.		<input type="checkbox"/>
52	US 4694416 A	19870915	VLSI programmable digital signal processor	708/408	Wheeler, James E. et al.		<input type="checkbox"/>